DHANALAKSHMI SRINIVASAN ENGINEERING COLLEGE

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

E-Spectra' 16 - News Letter

Jul – Dec 2016   **Volume – II, Issue - I**

About E-Spectra'16

DSEC, Department of Electronics & Communication Engineering is proud to publish its **NEWS LETTER 2016 - 17** (Volume - II, Issue – I).

The objective of this News Letter is to keep our Students, Parents, Faculty and Industry informed about the activities happening in the Department.

With the help of our association, we release the e-copy of biannual newsletter, **E-Spectra'16** comprising the details of our student's activities and faculty activities.

It encloses the activities such as Professional Society activities, Conferences attended and paper published in International and National journals by staff members and competitions won by the students and innovative projects carried out by them.

I am sure the college will scale upto greater heights in the years to come and serve many more in the society.

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Dear Readers, Kindly send your feedback about E-Spectra'16 News letter to ecedsec@gmail.com

DEPARTMENT PROFILE

The department of Electronics and Communication Engineering was established in the year 2001 during which the undergraduate programme was introduced. Post graduate programme was established in the year 2007 and it offers communication systems.

Under the UG programme, the total sanctioned student strength is 60 and now it is increased 120 to 180 in the Academic Year (2012-2013). Under the PG programme, the total sanctioned student strength is 18 and now it is increased to 36. The department is accredited by NBA and received an AICTE fund of Rs: 1,25,000 in the academic year of 2009-2010 and Rs: 5,00,000 in the academic year of 2011-2012 for MODROBS.

The department has highly qualified, committed and research oriented faculty members and supported by trained technical non-teaching staff. The department enjoys the privilege of having spacious and well-equipped laboratories that provide students, the industrial environment much needed for real life training.

The department has very close interaction with many leading industries in the country and research laboratories. Guest lectures, workshops, Industrial Visits, In-plant training, seminars, symposia, in-house projects in collaboration with well-established companies and other personality development that cater to the quest of today's society as well as student professionals.

The department shares the mission and vision of the institution, in imparting high quality education to the students.

Vision

To be a centre of repute for higher learning and research to cater the knowledge in Electronics and Communication field to the ever growing needs of industries and to facilitate the transformation of students into good human beings.

Mission

M1: Develop life-long learning skills that allow them to be adaptive and responsive to changes in society, technology and the environment, as well as career demands

M2: Promote a research activity through constant interaction with research organizations and industries

M3: Instigate our students to become responsible citizens and competent professionals with high ethical values

M4: Enable students to develop skills to solve complex technological problems of time and also provide a framework for promoting collaborative and multidisciplinary activities

Program Educational Objectives (PEOs)

PEO1: An in-depth knowledge and demonstrations related to the core area of Electronics and Communication Engineering, starting from the basics to the level of analysis, synthesis and design of circuits and systems, in addition to the exposure on latest advancements in the field

PEO2: Knowledge of recent design trends and adapt to new technologies through lifelong learning

PEO3: Technical knowledge, ethical values for professional development of the student to solve complex problems and to work in multi-disciplinary ambience, whose solutions lead to significant societal benefits

PEO4: Motivation to pursue higher studies so that they can contribute to the teaching profession, research and development of Electronics and Communication Engineering

Program Specific Outcomes (PSOs)

PSO1: **Fabrication of Electronic Components:** Graduates of the program will design a hardware model in real time applications using embedded technology and fabricate electronic equipment used in communication industries

PSO2: **Usage of Tools:** Graduates can exploit tools like Xilinx, Tanner, IE3D, Labview, Matlab, ModelSim, Keil and OrCAD to meet desired specifications with realistic constraints such as manufacturability and sustainability

GUEST LECTURE
25.07.2018

RTOS by Mr.C.Ravichandran
AUC – Ariyalur

GUEST LECTURE
23.08.2018

Analysis of Large Signal Amplifiers by
Mrs.P.Shanmugapriya
MAM College of Engineering,
Trichy

WORKSHOP on 22nd Sep 2016

Department of Electronics and Communication Engineering conducted one day **"National Level Workshop on Embedded and Real Time Systems" i**n the college Auditorium. The Chief Guest **Mr.A.DINESH and Mr.A.SUNDAR, Pantech Solutions** discussed the developments in embedded systems and basics of Keil software. He also encouraged the students to do research works.

INDUSTRIAL VISIT

Visit place: Vikram Sarabhai Space Centre, Trivandrum, Kerala, Focus Infotech, Ernakulum, Kerala

No. of Students – 92

INPLANT TRAINING

S. No.	Student Name	Place	Date
1.	P. Akshayaa	BSNL, Trichy	16.12.2016 to 29.12.2016 (14 Days)
2.	K.Ramya		
3.	S.Rishya Sithiravel		
4.	S.Sahithya Sithiravel		
5.	S.Swetha shree		
6.	R.Aarthi		
7.	K.Ashwathy	NCCT, Chennai	19.12.2016 to 30.12.2016 (12 Days)
8.	A.Jasmine		
9.	S.Kowsalya		
10.	Patrick Jose		
11.	J.Muthusamy		
12.	R.Vivek		

PROFESSIONAL SOCIETY ACTIVITIES – IAENG & ISTE

Placement talk & PDP – 09.07.2016

STUDENT ACHIEVEMENTS

S. No.	Student Name	Year	Event Name	Venue
1.	ASHWATHY. K	Workshop on "Digital Signal Processing"	School of Electrical Engineering, VIT University	12.08.2016 & 13.08.2016
2.	BHUVANESHWARI.N	Workshop on "Digital Signal Processing"	School of Electrical Engineering, VIT University	12.08.2016 & 13.08.2016
3.	INDRA PRIYADHASHINI.J	Workshop on "Digital Signal Processing"	School of Electrical Engineering,VIT University	12.08.2016 & 13.08.2016
4.	KOWSALYA.S	Workshop on "Digital Signal Processing"	School of Electrical Engineering,VIT University	12.08.2016 & 13.08.2016
5.	ASHWATHY. K	National Level Technical Festival Neithal'16 (Paper presentation)	CK College of Engineering & Technology	31.08.2016
6.	GAYATHRI.K	National Level Technical Festival Neithal'16 (Paper presentation)	CK College of Engineering & Technology	31.08.2016
7.	MOHANA.M	National Level Technical Festival Neithal'16 (Paper presentation)	CK College of Engineering & Technology	31.08.2016
8.	MONIKA.S	National Level Technical Festival Neithal'16 (Paper presentation)	CK College of Engineering & Technology	31.08.2016
9.	S. BALAGANESAN	One Day ISF Sponsored Seminar	K.Ramakrishna college of Engineering	16.08.2016
10.	BALAMURUGAN K	One Day ISF Sponsored Seminar	K.Ramakrishna college of Engineering	16.08.2016
11.	SATHISH A	One Day ISF Sponsored Seminar	K.Ramakrishna college of Engineering	16.08.2016
12.	SAM EBENEZER S G	One Day ISF Sponsored Seminar	K.Ramakrishna college of Engineering	16.08.2016
13.	KUMAR N	One Day ISF Sponsored Seminar	K.Ramakrishna college of Engineering	16.08.2016
14.	ALWIN RAJ S	One Day ISF Sponsored Seminar	K.Ramakrishna college of Engineering	16.08.2016

S. No.	Student Name	Year	Event Name	Venue
15.	VIJAYAKUMAR.S	One Day ISF Sponsored Seminar	K.Ramakrishna college of Engineering	16.08.2016
16.	ANBAZHAGAN R	One Day ISF Sponsored Seminar	K.Ramakrishna college of Engineering	16.08.2016
17.	KARTHIKEYAN C	One Day ISF Sponsored Seminar	K.Ramakrishna college of Engineering	16.08.2016

FACULTY PUBLICATIONS

S. No.	Name of the Faculty	Title Name	Journal Name
1	Mr.S.Sathyamoorthy Mrs.S.S.Sathya	Novel approach for Embedding data in audio signal using power control acoustic OFDM	Indo American Journal of Multidisciplinary Research and Review
2	Mrs.M.Dhayalini Ms.D.Sathyakala	Novel approach for the design of low power 128 bit SORT-CSLA design	International Journal of Computational Research and Development
3	Mr.T.Boopathy Ms.K.Suganya	Design of blind channel and source estimation in MIMO-OFDM system	International Journal of Computational Research and Development
4	Mrs.P.Rajeswari Mr.V.Prabhakaran	Design of compact micro-strip antenna for various mobile applications	International Journal of Advanced Trends in Engineering and Technology
5	Mr.V.Vijayakumar Mrs.S.Nithya	Design of scalable HEVC-DCT architecture for video coding applications	International Journal of Applied and Advanced Scientific Research
6	Mrs.S.S.Sathya Ms.J.Maria Sofi Anusuya	Monitoring and fault analysis in turbine using CAN bus	International Journal of Current Research and Modern Education
7	Mr.A.Yogeshwaran Mr.P.Kannan	Design and implementation of retinal and liver segment for medical diagnosis	International Journal of Current Research and Modern Education
8	Mr.N.Khadar Basha Mr.S.Sathyamoorthy	Survey of Classifiers using Medical Image Processing	Indo American Journal of Multidisciplinary Research and Review

TRAINING AND PLACEMENT

M.ABINAYA   **R. ANANDU**   **N. BHUVANESWARI**

C. RAJALAKSHMI   **C.SANDHYA**   **R.SAROJINI**

A.THANGASABARISWARAN   **K.V. VAISHNAVI**   **VIVEKIYA**

09 students got placed in Hindhuja Global Solutions, Chennai during the academic year 2017- 2018

Odd semester through On Campus Interview.